

BEST AVAILABLE COPY

2621
PTO/SB/21 (08-03)

Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paper Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application Number	09/677,227
		Filing Date	9/29/2000
		First Named Inventor	Najand, Shahriar
		Examiner Name	Unknown
		Art Unit	Unknown
Total Number of Pages in This Submission	17	Attorney Docket Number	0190148

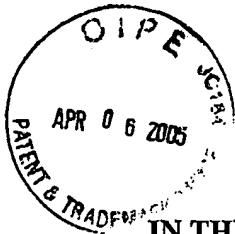
ENCLOSURES <i>(check all that apply)</i>			
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group	
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences	
<input type="checkbox"/> Amendment / Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information	
<input type="checkbox"/> Affidavits/declaration(s)	<input checked="" type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter	
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):	
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Request for Refund	1. Conexant Assignment (6 pages) 2. Pictos Assignment (8 pages) 3. Postcard	
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> CD, Number of CD(s) _____		
<input type="checkbox"/> Certified Copy of Priority Document(s)			
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application			
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53			
Remarks			

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Farshad Farjami, Esq., Reg. No. 41,014 Farjami & Farjami, LLP
Signature	
Date	3/31/05

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.	
Type or printed name	Lesley L. Lam
Signature	
	Date 4/4/2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

In you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.



Application Serial No.: 09/677,227
Attorney Docket No.: 0190148

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Najand, Shahriar	Group Art Unit: Unknown
Application Serial No.: 09/677,227	Examiner: Unknown
Filed: September 29, 2000	
Title: Digital Image Cross Talk Correction	Customer Number: <u>25700</u>

REVOCATION AND POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

Applicant of the above-identified patent application hereby revokes all previous powers of attorney given in this application and appoints Michael Farjami, Reg. No. 38,135; Farshad Farjami, Reg. No. 41,014, both attorneys, with full power of substitution and revocation, to prosecute this application, to make alterations and amendments thereto, to receive communications from the Patent and Trademark Office and to transact all business in the Patent and Trademark Office connected therewith.

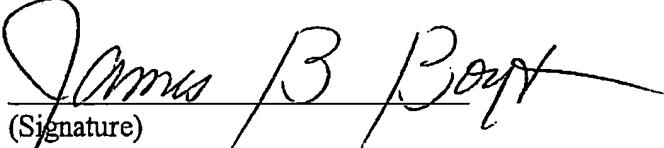
Please direct all communications to:

Farshad Farjami, Esq.
FARJAMI & FARJAMI LLP
26522 La Alameda Ave., Suite 360
Mission Viejo, California 92691
Tel: (949) 282-1000
Fax: (949) 282-1002

Customer Number: 25700

ESS Technologies International, Inc., certifies that it is the assignee of the entire right, title, and interest in the patent application identified above, as shown in the attached copy of assignment from Conexant Systems, Inc. to Pictos Technologies, Inc., and a copy of assignment from Pictos Technologies, Inc. to ESS Technologies International, Inc. The undersigned, whose title is supplied below, is authorized to act on behalf of assignee (37 CFR §3.73(b)).

Signed this 29th day of March, 2005



(Signature)
On behalf of ESS Technologies International, Inc.

James B. Boyd
(Print Name)

Chief Financial Officer
(Title)

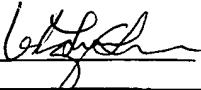
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service "First Class Mail Post Office to addressee" under 37 C.F.R. Sec. 1.10 addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on 4/14/2005.

Name

LESLEY L. LAM

Signature



APPENDIX F
PATENT ASSIGNMENT
UNITED STATES PATENT ASSIGNMENT

This Assignment is made as of the _____ day of _____, 2002 by CONEXANT SYSTEMS, INC, a Delaware corporation having a principal place of business at 4311 Jamboree Road, Newport Beach, California 92660 (hereinafter referred to as the "ASSIGNOR"), to PICTOS TECHNOLOGIES, INC., a Delaware corporation having a principal place of business at _____ (hereinafter referred to as the "ASSIGNEE").

WHEREAS, the ASSIGNOR is the owner of those U.S. patents and patent applications listed on Schedule A attached hereto (hereinafter collectively referred to as the "PATENTS"); and

WHEREAS, the ASSIGNEE is desirous of acquiring the entire right, title, and interest of ASSIGNOR in and to said PATENTS.

NOW, THEREFORE, for good and valuable consideration paid by the ASSIGNEE pursuant to that certain Formation Agreement between ASSIGNOR and ASSIGNEE and third parties dated May 3, 2002, receipt of which is hereby acknowledged, the ASSIGNOR does hereby agree as follows:

1. Assignment. ASSIGNOR hereby sells, assigns, transfers, and sets over to the ASSIGNEE and its successors and assigns all of ASSIGNOR's worldwide right, title, and interest in and to the PATENTS, including, without limitation, all (i) reissues, divisions, renewals, extensions, provisionals, continuations, and continuations-in-part of the PATENTS, (ii) causes of action, and enforcement rights for the PATENTS, including all rights to pursue damages, injunctive relief, and other remedies for infringement of the PATENTS, and (iii) rights to apply in any or all countries of the world for patents, certificates of invention, or other governmental grants for the PATENTS, including without limitation under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding. Notwithstanding the foregoing, ASSIGNOR expressly reserves and retains any rights ASSIGNOR may have to assert claims and recover damages for infringement of any of the PATENTS that occurred prior to July 1, 2002.

2. Authorization. ASSIGNOR also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents or certificates of invention which may be granted upon any of the PATENTS in the name of ASSIGNEE, as the ASSIGNEE to the entire interest therein.

3. Miscellaneous. The terms and conditions of this Assignment shall inure to the benefit of ASSIGNEE, its successors, assigns and other legal representatives, and shall be binding upon ASSIGNOR, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF, the ASSIGNOR has caused this Assignment to be executed by a duly authorized officer.

CONEXANT SYSTEMS, INC.

By:

Date:

Tobi Marshall

July 1, 2002

ACKNOWLEDGEMENT

STATE OF California)
COUNTY OF Orange) SS

Be it remembered on this 1 day of July, 2002, before me, the subscriber, a Notary Public authorized to take acknowledgements and proofs in said county and state, personally appeared SCOTT BLOOM, to me known, who being by me duly sworn according to law, on his/her oath does depose and make proof of my satisfaction that he/she was at the time of the execution thereof the CAO of Conexant Systems, Inc., the assignor in the foregoing instrument; and that he/she signed, sealed and delivered the instrument as the voluntary act and deed of said corporation, by virtue of his/her authority.



Tobi Marshall
Notary Public

SCHEDULE A
PATENTS

Docket No.	Patent No. Serial No.	Filing/Issue Date
97RSS423	[09/042,124] 6,256,350	3/13/1998 7/3/2001
92SC044	5,440,079 (08/080,805)	6/21/1993 8/8/1995
93SC029	5,381,054 [08/163,315]	12/7/1993 1/10/1995
94E078	5,706,369 (456,137)	5/31/1995 1/6/1998
94SC003	5,572,074 (469,989)	06/06/1995 11/05/1996
94SC082	5,502,299 [354,348]	12/12/1994 03/26/1996
95SC097	5,892,540 [08/662,362]	06/13/1996 04/06/1999
96E083	5,932,875 [08/888,817]	07/07/1997 08/03/1999
96SC028 98RSS221	5,929,434 [08/910,342]	08/13/1997 07/27/1999
98RSS353	6,153,955 [09/283,175]	4/1/1999 11/28/2000
97SC088	09/081,541	5/19/1998
00CXT0653I PRO	[60/159,000]	10/7/1999
00CXT0653I	[09/679,854]	10/5/2000
00CXT0652I	[09/188,831]	11/3/1998
00CXT0651I	[09/188,871]	11/9/1998
00CXT0650I	[09/267,337]	3/12/1999
00CXT0649I	[09/188,996]	11/9/1998
00CXT0648I PRO	[60/192,008]	3/24/2000
00CXT0648I	09/816,038	3/22/2001
00CXT0520I	[09/680,041]	10/5/2000
00CXT0456I	[09/679,857]	10/5/2000
00CXT0442I	09/904,980	7/12/2001

Docket No.	Parent No./ Serial No.	Filing/Issue Date
00CXT0441I	09/823,882	3/31/2001
00CXT0435I	09/795,033	2/26/2001
00CXT0433I	[09/733,788]	12/7/2000
00CXT0429I	[09/731,640]	12/7/2000
00CXT0427I	09/801,401	3/7/2001
00CXT0426I	09/815,584	3/23/2001
00CXT0422I	[09/672,987]	9/29/2000
00CXT0355T	09/885,433	6/18/2001
00CXT0340I	[09/677,227]	9/29/2000
00CXT0320T	[09/680,037]	10/5/2000
00CXT0319T	[09/680,036]	10/5/2000
00CXT0318I	[09/676,538]	9/29/2000
00CXT0299T	(09/823,941)	3/31/2001
00CXT0248T	09/900,732	6/6/2001
00CXT0209I	09/882,576	6/14/2001
99SC027	[09/468,696]	12/21/1999
99RSS495	09/742,786	12/20/2000
99RSS250	[09/408,454]	9/30/1999
99RSS249	[09/410,210]	9/30/1999
99RSS178	09/852,397	5/9/2001
99RSS168	09/239,925	5/9/2001
99RSS060	[09/295,699]	4/21/1999
98SC106	[09/408,919]	9/30/1999
98RSS367	[09/557,454]	4/24/2001



Docket No.	Patent No./ Serial No.	Filing/Issue Date
98RSS148	[09/407,501]	9/28/1999
98RSS147	[09/407,556]	9/28/1999
98RSS146	[09/407,741]	9/28/1999
98RSS144	[09/409,525]	9/30/1999
98RSS142	[09/407,395]	9/28/1999
98RSS139	[09/408,810]	9/29/1999
98RSS133	[09/371,491]	8/10/1999
98RSS100	[09/110,812]	7/6/1998
97SC089	[09/149,937]	9/9/1998
97SC089 CIP	[09/164,923]	10/1/1998
97SC087	[09/057,423]	4/8/1998
97SC033	[09/057,202]	4/8/1998
97SC033	[09/268,913]	3/16/1999
97RSS521	[09/676,551]	9/29/2000
97RSS519	[09/676,998]	9/29/2000
97RSS517	[09/062,343]	4/17/1998
97RSS476	[09/538,889]	3/30/2000
97RSS467	[09/408,198]	9/29/1999
97RSS320	[09/056,573]	4/7/1998
97RSS305	[09/042,142]	3/13/1998

Docket No	Patent No / Serial No	Filing/Issue Date
97RSS184	[09/034,819]	3/4/1998
96E096	[09/406,964]	9/28/1999
01CXT0217I	09/977,444	10/15/2001
00CXT0610I	10/004,909	11/2/2001
00CXT0467I	09/944,938	8/30/2001
00CXT0465I	09/982,540	10/17/2001
00CXT0450I	09/935,231	10/22/2001
00CXT0415I	09/949,688	9/5/2001

ASSIGNMENT FOR PATENT

WHEREAS:

Pictos Technologies, Inc. a corporation organized and under the laws of the State of Delaware, having the address of 4311 Jamboree Road Newport Beach, CA 92660

(hereinafter referred to as ASSIGNOR(S)), owns an interest in, to and under inventions listed in Appendix A, and in, to and under Letters Patent or similar legal protection to be obtained therefore in the United States and in any and all foreign countries for which applications for Letters Patent of the United States have been filed on dates listed in Appendix A, and

WHEREAS:

ESS Technologies International, Inc., a corporation organized and under the laws of the Cayman Islands having a place of business at 48401 Fremont Blvd, Fremont, CA 94538

(hereinafter referred to as ASSIGNEE), is desirous of acquiring ASSIGNOR'S entire interest in, to and under said inventions and in, to and under Letters Patent or similar legal protection to be obtained therefore in the United States and in any and all foreign countries.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN:

Be it known that in consideration of good and valuable consideration, the receipt of which is hereby acknowledged, ASSIGNOR(S) hereby sells, assigns and transfers to ASSIGNEE, its successors, legal representatives and assigns, the full and exclusive right, title and interest to said discoveries or inventions in the United States and its territorial possessions and in all foreign countries and to all Letters Patent or similar legal protection in the United States and its territorial possessions and in any and all foreign countries to be obtained for said invention by said application or any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in a foreign country for the full term or terms for which the same may be granted.

I, SAID ASSIGNOR(S), hereby authorize and request the Commissioner of Patents and Trademarks of the United States of America and any Official of any country or countries foreign to the United States of America whose duty it is to issue Letters Patent on applications as aforesaid, to issue all such Letters Patent for said discoveries or inventions to the ASSIGNEE, as assignee of the entire right, title and interest in, to and under the same, for the sole use and behalf of the ASSIGNEE, its successors, legal representatives and assigns, in accordance with the terms of this instrument.

I, SAID, ASSIGNOR(S), hereby covenant that I have full right to convey the entire right, title and interest herein sold, assigned, transferred and set over;

AND I, SAID ASSIGNOR(S) hereby further covenant and agree that the ASSIGNEE, its successors, legal representatives, or assigns, may apply for foreign Letters Patent on said discoveries or inventions and claim the benefits of the International Convention, and that I will, at any time, when called upon to do so by the ASSIGNEE, its successors, legal representatives, or assigns, communicate to the ASSIGNEE, its successors, legal representatives, or assigns, as the case may be, any facts known to me respecting said discover or invention, and execute and deliver and all lawful papers that may be necessary or desirable to perfect the title to the said discoveries or inventions, the said applications and the said Letters Patent in the ASSIGNEE, its successors, legal representatives and assigns, and that it reissues of the said Letters Patent or disclaimers relating thereto, or divisions, continuations, or re-filings of the said applications, or any thereof, shall hereafter be desired by the ASSIGNEE, its successors, legal representatives, or assigns, sign all lawful papers, make all rightful oaths, execute and deliver all such disclaimers and all divisional, continuation and reissue applications so desired, and do all lawful acts requisite for the application for such reissues and the procuring thereof and for the filing of such disclaimers and such applications, and generally do everything possible to aid the ASSIGNEE, its successors, legal representatives and assigns, to obtain and enforce proper patent protection for said invention or discover in all countries, and without further compensation but at the expense of the ASSIGNEE, its successors, legal representatives and assigns.

Assignor's signature:

Fred S.L. Chan

Citizenship:

USA

IN WITNESS WHEREOF, I have hereunto set my hand and affixed my seal this 25 day of JUNE,
2004.

STATE OF California

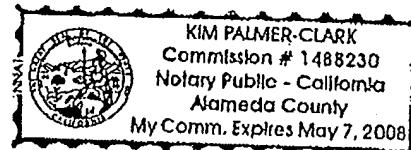
ss.:

COUNTY OF Alameda

On this 25 day of June, 2004 before me, the undersigned authority, personally appeared to me known and known to me to be the individual who is described in and who executed the foregoing Assignment, and who duly acknowledged to me that he executed the same as his own voluntary act and deed for the uses and purposed therein specified.

Kim Palmer-Clark

Notary Public



APPENDIX A
ISSUED PATENTS

Patent No.	Title	Inventor	Reel/Frame No.	Date of Recordation
5,381,054	Multiple input comparator circuit for a switched resistive network	Standley; David L.	013699/0267	01/29/2003
5,440,079	Object-background discrimination using analog VLSI circuit	Mathur , et al.	013699/0267	01/29/2003
5,502,299	Current ratio circuit for multi-color imaging	Standley; David L.	013699/0267	01/29/2003
5,572,074	Compact photosensor circuit having automatic intensity range control	Standley; David L.	013699/0267	01/29/2003
5,572,643	Web browser with dynamic display of information objects during linking	Judson; David H.	011911/0220 (ZING)	06/19/2001
5,706,369	Base-n resolution converter	Wang , et al.	013699/0267	01/29/2003
5,892,540	Low noise amplifier for passive pixel CMOS imager	Kozlowski , et al.	013699/0267	01/29/2003
5,929,434	Ultra-low noise high bandwidth interface circuit for single-photon readout of photodetectors	Kozlowski , et al.	013699/0267	01/29/2003
5,932,875	Single piece integrated package and optical lid	Chung , et al.	013699/0267	01/29/2003
6,040,567	Method and device for controlling fast periodic motion	Neher , et al.	013496/0589	08/14/2002
6,153,955	Implementing comprehensive PID engine with single bit adder	Cheung , et al.	013496/0589	08/14/2002
6,256,350	Method and apparatus for low cost line-based video compression of digital video stream data	Bishay , et al.	013496/0589	08/14/2002
6,271,884	Image flicker reduction with fluorescent lighting	Chung , et al.	013496/0589	08/14/2002

Patent No.	Title	Inventor	Reel/Frame No.	Date of Recordation
6,305,853	Camera utilizing film and reflective imager	Bishay , et al.	013496/0589	08/14/2002
6,437,826	Digital video teleconferencing camera system having a base	Arnold; Thomas A.	013209/0732	08/22/2002
6,441,453	Clear coating for digital and analog imagers	Tindle; Gary D.	011805/0861 (CONEXANT)	05/09/2001
6,441,857	Method and apparatus for horizontally scaling computer video data for display on a television	Wicker , et al.	013496/0589	08/14/2002
6,462,781	Foldable teleconferencing camera	Arnold; Thomas A.	013209/0732	08/22/2002
6,486,522	Light sensing system with high pixel fill factor	Bishay , et al.	013496/0589	08/14/2002
6,493,030	Low-noise active pixel sensor for imaging arrays with global reset	Kozlowski , et al.	013496/0589	08/14/2002
6,498,331	Method and apparatus for achieving uniform low dark current with CMOS photodiodes	Kozlowski , et al.	013496/0589	08/14/2002
6,507,364	Edge-dependent interpolation method for color reconstruction in image processing devices	Bishay , et al	012273/0217 (CONEXANT)	11/05/2001
6,532,040	Low-noise active-pixel sensor for imaging arrays with high speed row reset	Kozlowski , et al.	012273/0217 (CONEXANT)	11/05/2001
6,534,796	Integrated circuit optics assembly unit	Bishay , et al.	013496/0589	08/14/2002
6,535,247	Active pixel sensor with capacitorless correlated double sampling	Kozlowski , et al.	013496/0589	08/14/2002
6,563,363	Switched capacitor comparator network	Tay; Hiok-Nam	013851/0225	03/17/2003
6,580,456	Programmable timing generator	Jacobs; William S.	009594/0366 (SIERRA IMAGING)	11/09/1998
6,587,142	Low-noise active-pixel sensor for imaging arrays with high speed row reset	Kozlowski , et al.	013496/0589	08/14/2002

Patent No.	Title	Inventor	Reel/Frame No.	Date of Recordation
6,593,607	Image sensor with enhanced blue response and signal cross-talk suppression	Hsieh; Biay-Cheng	013496/0589	08/14/2002
6,597,394	Programmable image transform processor for digital image processing	Duncan , et al.	009591/0524 (SIERRA IMAGING)	11/09/1998
6,617,562	CMOS imager with discharge path to suppress reset noise	Mann; Richard A.	011232/0239 (CONEXANT)	10/05/2000
6,639,204	Solid state color imager and method of manufacture	Mann; Richard A.	013851/0225	03/17/2003
6,677,996	Real time camera exposure control	Chung , et al.	013496/0589	08/14/2002
6,697,111	Compact low-noise active pixel sensor with progressive row reset	Kozlowski , et al.	013851/0225	03/17/2003
6,744,032	Arrangement of microlenses in a solid state image sensor for improving signal to noise ratio	Tay; Hiok-Nam	013851/0225	03/17/2003
6,617,562	CMOS imager with discharge path to suppress reset noise	Mann; Richard A.	011232/0239	10/05/2000

PATENT APPLICATIONS

Application No.	Title	Inventor	Reel/Frame No.	Date of Recordation
09/034,819	Method and apparatus for compensating for geometric distortion caused by a lensing system in a digital image detector	Pine, Joshua I.	010885/0931	06/02/2000
09/062,343	CMOS imaging apparatus	Ferry et al.		04/17/1998 (filed)

Application No.	Title	Inventor	Reel/Frame No.	Date of Recordation
09/268,913	Low noise CMOS active-pixel sensor for imaging arrays with high speed global or row reset	Kozlowski et al.		03/16/1999 (filed)
09/371,491	Imager with orientation correction capabilities	Pine, Josh I.	013496/0589	08/14/2002
09/407,395	Color imager without filter	Bishay et al.		9/28/1999 (filed)
09/407,501	An integrated camera module	Bishay et al.	013496/0589	08/14/2002
09/407,556	Hybrid multiple sensor device	Bishay et al.	013496/0589	08/14/2002
09/408,198	Infrared communication system utilizing receiver with multiple photo-sensors	Chung, Randall M.	013496/0589	08/14/2002
09/410,210	Active pixel sensor with multiplexed photosensing elements readout scheme	Hsieh, Biay-Cheng		09/30/1999 (filed)
09/538,889	Automatic gain control algorithm for pc-based video camera	Dong, Blake, M.	013496/0589	08/14/2002
09/557,454	CMOS JFET amplified pixel	Kozlowski et al.	013496/0589	08/14/2002
09/672,987	Selectable resolution image capture system	Pine, Joshua I.	013496/0589	08/14/2002
09/676,538	Combined digital image across talk correction and interpolation	Najand, Shahriar	011178/0767	09/29/2000
09/676,551	NO FILE			
09/676,998	Exposure control in electromechanical imaging devices	Pine, Joshua I.	013496/0589	08/14/2002
09/677,227	NO FILE			
09/679,854	NO FILE			
09/680,036	NO FILE			
09/731,640	Imaging system for minimizing pixel defects	Pine, Joshua I.	013496/0589	08/14/2002
09/733,788	Enhanced resolution mode using color image capture device	Pine, Joshua I.	013496/0589	08/14/2002

Application No.	Title	Inventor	Reel/Frame No.	Date of Recordation
09/742,786	Automatic detection and correction of pixel defects in solid state imagers	Pine, Joshua I.	013496/0589	08/14/2002
09/795,033	Imaging system having selectable interpolation processing	Pine, Joshua I.	013496/0589	08/14/2002
09/801,401	Imaging system having an image memory between the functional processing system	Pine, Joshua I.	013496/0589	08/14/2002
09/815,584	Imaging system having adaptive clocking in response to processing state	Pine, Joshua I.	013496/0589	08/14/2002
09/823,941	NO FILE			
09/852,397	Chip On Board (COB) package for CMOS Imager	Tindle et al.	013496/0589	08/14/2002
09/882,576	NO FILE			
09/935,213	NO FILE			
09/935,231	Semiconductor device for isolating a photodiode to reduce junction leakage and method of formation	Mann, Richard A.	013851/0225	03/17/2003
09/949,688	Off-grid interpolation in image processing	Bao et al.	013496/0589	08/14/2002
09/977,444	NO FILE			
10/016,713	Method and article of manufacture for micro-lens resulting from multi-stage fabrication technique.	Bencuya, Selim S.	014081/0620	05/16/2003
10/057,731	NO FILE			
10/072,345	Imaging system combining multiple still images for higher resolution image output	Pine, Joshua I.		10/25/2001 (filed)
10/102,042	Efficient implementation of a noise removal filter	Pan, Shien-Tai	103022/0288	06/24/2002
10/102,105	Image resolution conversion using pixel dropping	Bao et al.		03/20/2002 (filed)
10/102,410	NO FILE			
10/113,545	NO FILE			
10/119,982	Tapered threshold reset FET for CMOS imagers	Kozlowski et al.	013851/0225	03/17/2003

Application No.	Title	Inventor	Reel/Frame No.	Date of Recordation
10/136,268	NO FILE			
10/136,413	Suppressing radiation charges from reaching dark signal sensor	Mann et al.	013851/0225	03/17/2003
10/293,510	Semiconductor device for isolating a photodiode to reduce junction leakage and method of formation	Mann, Richard A.	013851/0225	03/17/2003
60/376,690	NO FILE			
60/376,748	NO FILE			
60/376,750	NO FILE			
60/376,751	NO FILE			

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- BLACK BORDERS**
- IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- FADED TEXT OR DRAWING**
- BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- SKEWED/SLANTED IMAGES**
- COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- GRAY SCALE DOCUMENTS**
- LINES OR MARKS ON ORIGINAL DOCUMENT**
- REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- OTHER:** _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.